

# The SAMPA Schedule

- Initial schedule, p45 TDR-15
  - 1<sup>st</sup> component run Nov 2013
  - 2<sup>nd</sup> integration run Aug 2014
  - 3<sup>rd</sup> “final” run May 2015
- Updated schedule as told to LLNL
  - 1<sup>st</sup> component run July 2014
  - 2<sup>nd</sup> integration run Dec. 2014
    - we padded this to Jan/Feb in our request
    - a linear delay would place this run May, still in FY15

# LLNL path forward

- Procure/test SAMPA chips still worth doing
  - keep contact with group
  - discuss options for funding when ready (assume May 2015 as reasonable schedule)
  - continue talks with UC Davis on tests

# What of Gating Grid idea?

- primary motivation, to engage HW, still worth pursuing
- consider other options, for example, invitation to review design plans when ready
- without sufficient cause to revisit idea, drop gating grid as R&D goal